

PATENT

IN THE SPECIFICATION

Please amend the paragraph beginning on page 6, line 7 as follows:

--The CPU 11 is also coupled to a system memory 12, such as a random access memory 13. The CPU 11 may be coupled to a secondary nonvolatile storage device 17 by way of a system bus 14, such as a Peripheral component Interconnect (PCI) bus 14, for example and a host controller 16. The secondary nonvolatile storage device 17 may be a hard disk drive, a compact disk (CD) drive, a digital video disk (DVD) drive, a floppy disk drive, a Zip drive, a SuperDisk drive, a Magneto-Optical disk drive, a Jazz drive, a high density floppy (HiFD) drive, or other device or technology capable of preserving data in the event of a power-off condition. A video controller 15 is coupled to the CPU 11 and system bus 14 and is operative to control the transmission and presentation of information on a corresponding display device (not shown). --